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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Serge Lasserre (TI-31354)

Conf. No.: 4434

Serial No: 09/932,381

Examiner: Reginald G. Bragdon

Filed: August 17, 2001

Art Unit: 2188

For: Local Memory With Indicator Bits to Support Concurrent DMA and CPU Access

AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

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JUL 20 2004

Dear Sir:

Introductory Comments:

This paper is presented in response to the Office Action mailed on April 26, 2004. It is submitted that this paper is fully responsive to that Office Action. Reconsideration of this application, based on the following amendment and accompanying remarks is respectfully requested.

Amendment to the claims begin on page 2.

Applicant's remarks begin on page 8.

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CERTIFICATION OF FACSIMILE TRANSMISSION

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Rodney M. Anderson

July 20, 2004
Date

FACSIMILE COVER SHEET

☒ **FACSIMILE COVER SHEET**
☐ NEW APPLICATION
☐ DECLARATION
☐ ASSIGNMENT
☐ FORMAL DRAWINGS
☐ INFORMAL DRAWINGS
☐ CONTINUATION APP'N
☐ DIVISIONAL APP'N

☒ **AMENDMENT (12 Pages)**
☐ EOT
☐ NOTICE OF APPEAL
☐ APPEAL
☐ ISSUE FEE

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